

174542

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Date 12/16/05 Serial # 10/928427 Priority Application Date 12/5/03

Your Name M. Lewis Examiner #

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Claims 1-18

Problem. See Paragraphs 9-14

3d from " abstract "

2005/D27519

CMOS/Memory

Decrease pitch to provide more substrate area.

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S8 18543 S S6(3N)S7
S9 4 S S1 AND S8
S10 4 RD (unique items)
S11 4 S S1 AND S6 AND S3
S12 0 S S11 NOT S10
S13 515 S S8 AND S2 AND S3
S14 80 S S13 AND PD<=20031205
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S20 297 S S5(2N)(REGION? OR AREA OR SECTION? OR SEGMENT? OR PART????)
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